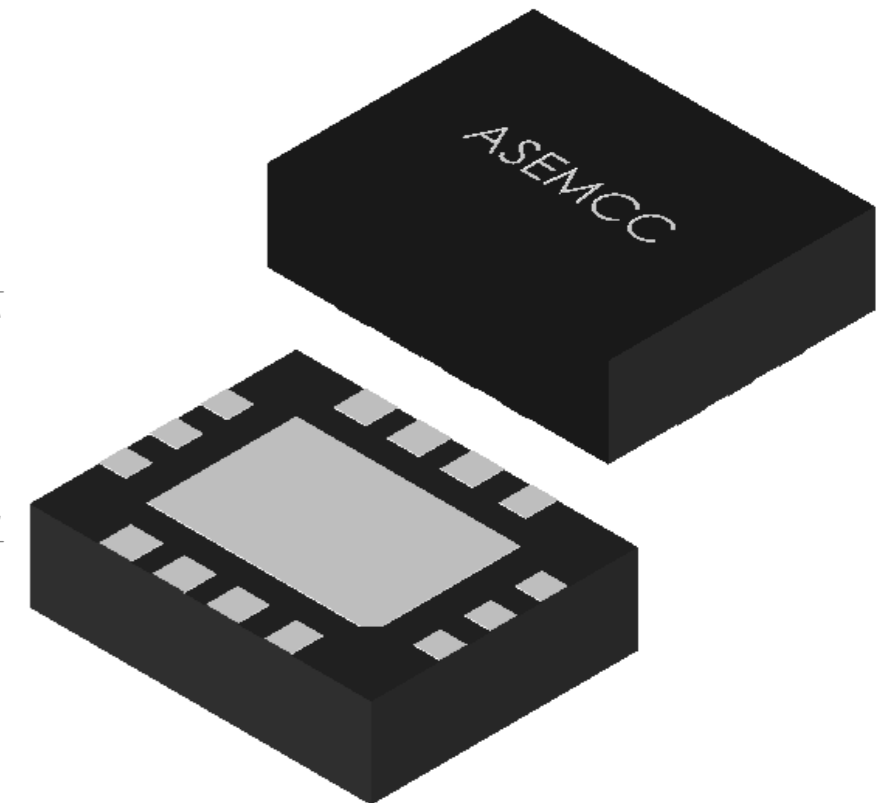
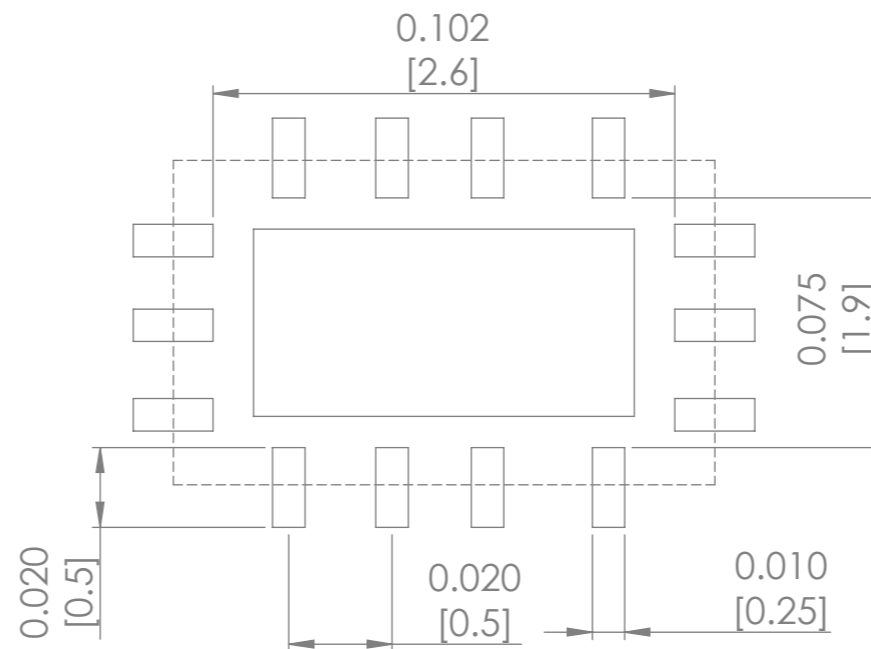



Pin	Pin Name	Pin Type	Function
1	Enable	I	Enables outputs when high and disables (tri-state) them when low
2	NC	NA	Leave unconnected or grounded
3	NC	NA	Leave unconnected or grounded
4	GND	Power	Ground
5	FS0	I	Least significant bit for frequency selection
6	FS1	I	Most significant bit for frequency selection
7	NC	NA	Leave unconnected or grounded
8	Output1	O	CMOS output 1
9	OS0	I	Least significant bit for output drive strength selection
10	OS1	I	Middle bit for output drive strength selection
11	NC	NA	Leave unconnected or grounded
12	VDD2	Power	Power Supply
13	VDD	Power	Power Supply
14	OS2	I	Most significant bit for output drive strength selection

Recommended Land Pattern



UNLESS OTHERWISE SPECIFIED: DIMENSIONS ARE IN MM SURFACE FINISH: TOLERANCES: LINEAR: ANGULAR:				FINISH:		DEBUR AND BREAK SHARP EDGES		DO NOT SCALE DRAWING		REVISION -	
DRAWN SAAZVAT				SIGNATURE		DATE		 30332 Esperanza, Rancho Santa margarita, California 92688			
CHK'D XXXXXX								TITLE: Oscillator			
APPV'D								DWG NO. ASEMCC A3			
MFG								SCALE:4:1 SHEET 1 OF 1			
Q.A						MATERIAL:					
						WEIGHT:					